



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/813,790	03/26/2004	Hooman Honary	1020.P18416	5212
57035	7590	09/11/2007	EXAMINER	
KACVINSKY LLC			LI, AIMEE J	
C/O INTELLEVATE				
P.O. BOX 52050			ART UNIT	PAPER NUMBER
MINNEAPOLIS, MN 55402			2183	
			MAIL DATE	DELIVERY MODE
			09/11/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No. 10/813,790	Applicant(s) HONARY ET AL.	
	Examiner Aimee J. Li	Art Unit 2183	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 16 July 2007.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1,2,4,5,7-10 and 12-24 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1,2,4,5,7-10 and 12-24 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. Claims 1-2, 4-5, 7-10, and 12-24 have been considered. Claim 11 has been cancelled as per Applicant's request. Claims 1, 10, 16, and 22 have been amended as per Applicant's request.

Papers Submitted

2. It is hereby acknowledged that the following papers have been received and placed of record in the file: RCE as filed 16 July 2007; Amendment as filed 16 July 2007; and Extension of Time for 1 Month as filed 16 July 2007.

Continued Examination Under 37 CFR 1.114

3. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 16 July 2007 has been entered.

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

5. Claims 1-2, 4-5, 7-10, and 12-24 are rejected under 35 U.S.C. 102(b) as being taught by Gove et al., U.S. Patent Number 5,212,777 (herein referred to as Gove).

6. Referring to claim 1, Gove has taught an apparatus, comprising:

Art Unit: 2183

- a. A memory unit to store data (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62);
- b. A plurality of parallel data paths to process said data (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62);
- c. A plurality of control units to control said data paths (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62); and
- d. A switch to connect said control units to said data paths, said switch to receive configuration information to establish a first set of connections between said control units and said data paths to execute a first process using single instruction multiple data processing, and a second set of connections between said control units and said data paths to execute a second process using multiple instruction multiple data processing (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to

Art Unit: 2183

column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62);

- e. Wherein said data paths are configured based upon said connections between said control units and said data paths to perform said first process and said second process in parallel (Gove column 25, lines 44-54; column 62, lines 40-52; column 63, lines 4-20; column 170, lines 50-62; and Figure 64 – In regards to Gove, the SIMD/MIMD Hybrid shown in Figure 64 shows that SIMD and MIMD processes performed in parallel with some of the data paths and controllers configured for SIMD operation and some are configured for MIMD operation.).

7. Referring to claim 2, Gove has taught the apparatus of claim 1, wherein each control unit controls execution of a single program instruction (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62).

8. Referring to claim 4, Gove has taught the apparatus of claim 2, wherein said first set of connections connect at least one of said plurality of control units to multiple data paths, with said one control unit to control said multiple data paths (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62).

9. Referring to claim 5, Gove has taught the apparatus of claim 4, wherein each data path performs a same set of operations using said data (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62).

10. Referring to claim 7, Gove has taught the apparatus of claim 2, wherein said second set of connections connect multiple control units to multiple data paths, with each control unit to control a single data path (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62).

11. Referring to claim 8, Gove has taught the apparatus of claim 4, wherein each data path performs a different set of operations using said data (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62).

12. Referring to claim 9, Gove has taught the apparatus of claim 1, further comprising a configuration module to configure said switch to establish said connections in accordance with said configuration information (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62).

Art Unit: 2183

13. Referring to claim 10, Gove has taught a system, comprising:
- a. An antenna (Gove column 5, lines 49-56; column 28, lines 63-64; column 66, lines 3-8; and Figure 48). In regards to Gove, antennas send and receive signals for devices, such as televisions and remote cameras (Please see www.dictionary.com “antenna” ©2000).
 - b. A host processing system (Gove column 2, line 66 to column 3, line 4; column 5, lines 20-34; column 6, lines 23-36; column 12, line 63 to column 13, line 9; Figure 11 Figure 2; Figure 4; Figure 17; and Figure 29);
 - c. A configuration module to store configuration information (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62); and
 - d. A reconfigurable communication architecture module to receive said configuration information, said reconfigurable communication architecture module to configure itself to perform single instruction multiple data processing in a first configuration to execute a first process, and to perform multiple instruction multiple data processing in a second configuration to execute a second process (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62);

- e. Wherein said routing elements are configured based upon said connections between said processing elements and said routing elements to perform said first process and said second process in parallel (Gove column 25, lines 44-54; column 62, lines 40-52; column 63, lines 4-20; column 170, lines 50-62; and Figure 64 – In regards to Gove, the SIMD/MIMD Hybrid shown in Figure 64 shows that SIMD and MIMD processes performed in parallel with some of the data paths and controllers configured for SIMD operation and some are configured for MIMD operation.).

14. Referring to claim 12, Gove has taught the system of claim 10, wherein one of said processing elements comprises:

- a. A memory unit to store data (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62);
- b. A plurality of parallel data paths to process said data (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62);
- c. A plurality of control units to control said data paths (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-

13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62); and

- d. A switch to connect said control units to said data paths, said switch to receive said configuration information to establish a first set of connections between said control units and said data paths to execute said first process, and a second set of connections between said control units and said data paths to execute said second process (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62).

15. Referring to claim 13, Gove has taught the system of claim 12, wherein each control unit controls execution of a single program instruction (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62).

16. Referring to claim 14, Gove has taught the system of claim 13, wherein said first set of connections connect at least one of said plurality of control units to multiple data paths, with said one control unit to control said multiple data paths (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62).

Art Unit: 2183

17. Referring to claim 15, Gove has taught the system of claim 13, wherein said second set of connections connect multiple control units to multiple data paths, with each control unit to control a single data path (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62).

18. Referring to claim 16, Gove has taught a method, comprising:

- a. Receiving configuration information at a switch (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62); and
- b. Configuring said switch to establish a first set of connections between a plurality of control units and a plurality of data paths to execute a first process using single instruction multiple data processing (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62); and
- c. Configuring said switch to establish a second set of connections between said control units and said data paths to execute a second process using multiple instruction multiple data processing (Gove column 1, line 47 to column 3, line 20;

column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62);

- d. Wherein said data paths are configured based upon said connections between said control units and said data paths to perform said first process and said second process in parallel (Gove column 25, lines 44-54; column 62, lines 40-52; column 63, lines 4-20; column 170, lines 50-62; and Figure 64 – In regards to Gove, the SIMD/MIMD Hybrid shown in Figure 64 shows that SIMD and MIMD processes performed in parallel with some of the data paths and controllers configured for SIMD operation and some are configured for MIMD operation.).

19. Referring to claim 17, Gove has taught the method of claim 16, wherein each control unit controls execution of a single program instruction (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62).

20. Referring to claim 18, Gove has taught the method of claim 17, wherein said first set of connections connect at least one of said plurality of control units to multiple data paths, with said one control unit to control said multiple data paths (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62).

Art Unit: 2183

21. Referring to claim 19, Gove has taught the method of claim 17, wherein said second set of connections connect multiple control units to multiple data paths, with each control unit to control a single data path (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62).
22. Referring to claim 20, Gove has taught the method of claim 16, further comprising:
- a. Receiving a first set of data (Gove column 8, line 42 to column 9, line 13; column 10, lines 5-8, 22-29, and 38-44; column 10, line 65 to column 11, line 30; column 61, line 60 to column 62, line 24; Figure 12; Figure 13; Figure 14; Figure 15; Figure 61; and Figure 62)
 - b. Storing said first set of data in a memory unit (Gove column 8, line 42 to column 9, line 13; column 10, lines 5-8, 22-29, and 38-44; column 10, line 65 to column 11, line 30; column 61, line 60 to column 62, line 24; Figure 12; Figure 13; Figure 14; Figure 15; Figure 61; and Figure 62); and
 - c. Processing said first set of data with said data paths using said first set of connections (Gove column 8, line 42 to column 9, line 13; column 10, lines 5-8, 22-29, and 38-44; column 10, line 65 to column 11, line 30; column 61, line 60 to column 62, line 24; Figure 12; Figure 13; Figure 14; Figure 15; Figure 61; and Figure 62).
23. Referring to claim 21, Gove has taught the method of claim 16, further comprising:

- a. Receiving a second set of data (Gove column 8, line 42 to column 9, line 13; column 10, lines 5-8, 22-29, and 38-44; column 10, line 65 to column 11, line 30; column 61, line 60 to column 62, line 24; Figure 12; Figure 13; Figure 14; Figure 15; Figure 61; and Figure 62);
 - b. Storing said second set of data in a memory unit (Gove column 8, line 42 to column 9, line 13; column 10, lines 5-8, 22-29, and 38-44; column 10, line 65 to column 11, line 30; column 61, line 60 to column 62, line 24; Figure 12; Figure 13; Figure 14; Figure 15; Figure 61; and Figure 62); and
 - c. Processing said second set of data with said data paths using said second set of connections (Gove column 8, line 42 to column 9, line 13; column 10, lines 5-8, 22-29, and 38-44; column 10, line 65 to column 11, line 30; column 61, line 60 to column 62, line 24; Figure 12; Figure 13; Figure 14; Figure 15; Figure 61; and Figure 62).
24. Referring to claim 22, Gove has taught an article comprising:
- a. A storage medium (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62);
 - b. Said storage medium including stored instructions that, when executed by a processor, result in receiving configuration information at a switch, configuring said switch to establish a first set of connections between a plurality of control units and a plurality of data paths to execute a first process using single

Art Unit: 2183

instruction multiple data processing, and configuring said switch to establish a second set of connections between said control units and said data paths to execute a second process using multiple instruction multiple data processing (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62);

- c. Wherein said data paths are configured based upon said connections between said control units and said data paths to perform said first process and said second process in parallel (Gove column 25, lines 44-54; column 62, lines 40-52; column 63, lines 4-20; column 170, lines 50-62; and Figure 64 – In regards to Gove, the SIMD/MIMD Hybrid shown in Figure 64 shows that SIMD and MIMD processes performed in parallel with some of the data paths and controllers configured for SIMD operation and some are configured for MIMD operation.).

25. Referring to claim 23, Gove has taught the article of claim 22, wherein the stored instructions, when executed by a processor, further result in said first set of connections connecting at least one of said plurality of control units to multiple data paths, with said one control unit to control said multiple data paths (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62).

Art Unit: 2183

26. Referring to claim 24, Gove has taught the article of claim 22, wherein the stored instructions, when executed by a processor, further result in said second set of connections connecting multiple control units to multiple data paths, with each control unit to control a single data path (Gove column 1, line 47 to column 3, line 20; column 5, lines 20-56; column 6, lines 6-43; column 7, lines 5-13; column 8, line 42 to column 9, line 13; column 16, lines 6-17; column 61, line 60 to column 62, line 24; Figure 1; Figure 2; Figure 4; Figure 14; Figure 15; Figure 17; Figure 61; and Figure 62).

Response to Arguments

27. Examiner withdraws the rejection of claims 1-2, 4-5, and 7-24 under 35 U.S.C. 112, second paragraph, in favor of the cancelled and amended claims.

28. Applicant's arguments filed 16 July 2007 have been fully considered but they are not persuasive. Applicant's argue in essence on pages 10-13

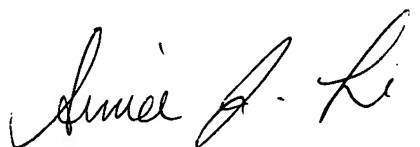
...the claimed subject matter discloses that running SIMD and MIMD processes in parallel is achieved by configuring the data paths based upon the connections between said data paths and the control units. Consequently, GOVE fails to disclose all the elements or features of the claimed subject matter...

29. This has not been found persuasive. In the cited portions of the above rejection, it is shown that Gove has taught a SIMD/MIMD hybrid functionality is possible with in his processor system. Gove further shows in Figure 64 the SIMD/MIMD hybrid. As shown in Figure 64, the SIMD/MIMD hybrid has some data paths working in SIMD mode while the other data paths work in MIMD mode. The data paths working in SIMD mode had all the data paths and

associated control units connected together, while the data paths and associated control units are connected so separate, individual instruction memories.

Conclusion

30. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Aimee J. Li whose telephone number is (571) 272-4169. The examiner can normally be reached on M-T 7:00am-4:30pm.
31. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie Chan can be reached on (571) 272-4162. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.
32. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.



Aimee J Li
Examiner
Art Unit 2183

3 September 2007